## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

LOW CAPACITANCE FET FOR OPERATION AT SUBTHRESHOLD VOLTAGES

Application Number:

**Confirmation Number:** 

First Named Applicant:

**Brent Anderson** 

Attorney Docket Number:

BUR920040100US1

Art Unit:

Examiner:

Search string:

( 6344660 or 5981970 or 5247212 or 4182023 ).pn

## **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
SA	1	6344660	2002-02-05	Dimitrakopoulos et al			
OA	2	.5981970	1999-11-09	Dimitrakopoulos et al			
	3	5247212	1993-09-21	Vinal			
120	4	4182023	1980-01-08	Cohen et al			

Signature

Examiner Name	Date
WWW//Smizm	10/10/85

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(Ose several sheets if necessar)			PATE I	7	Filing Date		Group Art Unit		
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	REF	DOCUMENT NUMBER	DATE		COUNTRY	CLASS	SUBCLASS	Translation YES NO	
M	>	GB 2090467A	07/07/1982	GB					
M	_	EP 0210625 A2	08/11/1987	ЕРО					
	<del>\</del>		OTHER !	DOCUMEN	TS (Including Aut	hor, Title, Date, l	Pertinent Pages, E	(c.)	
MA		R.C. Dockerty; "Forma 1743-1744	tion of Low Cap	acitance D	iffusions"; IBM Techni	cal Disclosur B	ulletin; August	1981; pgs.	
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